

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

Gustavson, et al.

Serial No.: 10/671,889

Group Art Unit: 2122

Filing Date: September 29, 2003

Examiner: Not Yet Assigned

For: METHOD AND STRUCTURE FOR PRODUCING HIGH PERFORMANCE  
LINEAR ALGEBRA ROUTINES USING PREFETCHING FOR KERNEL  
ROUTINES

Honorable Commissioner for Patents  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Sir:

Under the provisions of 37 CFR §1.97 through §1.98 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following documents listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. These references were cited by the Examiner in co-pending Application No. 10/671,933, in an Office Action dated December 19, 2006, and in co-pending Application No. 10/671,934, in an Office Action dated December 18, 2006. A copy of the listed documents are provided herewith for the convenience of the Examiner.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Submitted herewith is the IDS fee of \$180.00.

Please charge any deficiencies in fees and credit any overpayment of fees to  
Assignee's Deposit Account No. 50-0510.

Respectfully submitted,

Date: 3/30/07



Frederick E. Cooperrider, Esq.  
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# **INFORMATION DISCLOSURE CITATION**

(Use several sheets if necessary)

Docket Number (Optional)

**YOR920030170US1**

Application Number

**10/671,889**

Applicant(s)

**Gustavson, et al.**

Filing Date

**September 29, 2003**

Group Art Unit

**Not Yet Assigned**

## **U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		US-6,601,080	07-2003	Garg, Rajat P.			
		US-5,983,230	11-1999	Gilbert et al.			
--	--	US-7,031,994 <i>previously submitted</i>	04-2006	Lao et al.			

## **U.S. PATENT APPLICATION PUBLICATIONS**

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

## **FOREIGN PATENT DOCUMENTS**

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

## **OTHER DOCUMENTS**

(Including Author, Title, Date, Pertinent Pages, Etc.)

		Phillip et al., PLAPACK: Parallel Linear Algebra Package Design Overview, 1997, IEEE Proceedings of the ACM/IEEE SC97 Conference.
		Agarwal et al., A High Performance Algorithm Using Pre-Processing for the Sparse Matrix-Vector Multiplication, 1992, IEEE, pages 32-41.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.